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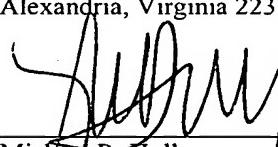
PATENT  
29926/38060

1156

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application of: Sung-Kwon Lee and )  
Sang-Ik Kim )  
Serial No.: 10/066,849 )  
Filed: February 4, 2002 )  
For: Method for Manufacturing Multi- )  
Level Interconnections with Dual )  
Damascene Process )  
Group Art Unit: 1756 )  
Examiner: John S. Ruggles )  
)

I hereby certify that this paper and the documents referred to as enclosed therewith are being deposited with the United States Postal Service as first class mail, postage prepaid, on December 29, 2003, in an envelope addressed to Commissioner for Patents, P.O. Box 1450 Alexandria, Virginia 22313-1450

  
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AMENDMENT

Commissioner for Patents  
P.O. Box 1450  
Alexandria, Virginia 22313-1450

Sir:

In response to the office action mailed on September 29, 2003, please amend the above-identified patent application as follows.